

Topics on Secure Architectures

TOSAR 2018

December 7-8, 2018, Iasi, Romania

Symposium organized by

“Gheorghe Asachi” Technical University of Iasi in cooperation with

The Romanian Chapter of IEEE Solid State-Circuits Society and IEICE Communications Society as technical co-sponsors

* An IEEE SSCS-037 scientific event *

Important dates

- ◆ Abstract Submission Deadline: **October 26, 2018**
- ◆ Notification of acceptance: **November 5, 2018**
- ◆ Pre-registration Deadline: **November 21, 2018**
- ◆ Extended Paper Submission Deadline: **December 2, 2018**
- ◆ Symposium days: **December 7-8, 2018**

Topics

- ◆ Hardware implementations for Security
- ◆ Cryptographic primitives and protocols
- ◆ Computer and Information Security

